Marvell 美满电子 2021 校园招聘

Essential technology, done right

关于我们: Marvell 成立于 1995 年,总部位于美国加州硅谷,全球员工近 6000 人,拥有全球专利 10000 余 项。Marvell 在美国、中国、印度、德国、以色列设有研发中心。Marvell 中国总部位于上海张江,并在北 京、南京、成都设有研发中心。

在 Marvell,我们坚信,基础设施是社会进步的重要推动力。我们认为,执行力与创新力同等重要。建立 紧密和广泛的合作是推动技术进步的基础。因此,Marvell的理念就是,与您携手并进。广泛合作为我们打 下坚实的基础,让一切进步皆有可能。

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招聘职位:版图设计 工作地点:南京 年薪:15~20w Job Description Be a part of the Central Analog Layout Team at Marvell china Main responsibility is perform analog layout and related drc/lvs verification, debug and fix violations Will be responsible for all levels of analog/mixed-signal layout from block level to AFE top level integration and physical verification

Qualifications

BS degree of electronic engineering, computer science Be familiar in high frequency analog/mixed-signal layout methods Be familiar with 16/12/7nm technology, experience in 5nm is preferred Be familiar with cadence virtuoso, synopsys laker and calibre drc/lvs Good communication skills in written and verbal English Good team work and communication skills. Hardworking and self-motivated under a high competition IP team Good understanding of layout impact on device matching, noise coupling from signal, supply and substrate Good understanding the importance of signal flow, power/ground structure and block placement in layout floorplan

Be familiar with SKILL/Perl/TCL program is preferred

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